

Electronic Patent Application Fee Transmittal

| Application Number: | 10716309 | | | |
|--------------------------------------|--|-----------------|---------------|-----------------------------|
| Filing Date: | 17-Nov-2003 | | | |
| Title of Invention: | TECHNIQUES FOR GROUPING CIRCUIT ELEMENTS INTO LOGIC BLOCKS | | | |
| First Named Inventor/Applicant Name: | Ketan Padalia | | | |
| Filer: | David Brian Raczkowski/Lisa Jeanetta | | | |
| Attorney Docket Number: | 015114-069200US | | | |
| Filed as Large Entity | | | | |
| Utility Filing Fees | | | | |
| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
| Basic Filing: | | | | |
| Pages: | | | | |
| Claims: | | | | |
| Miscellaneous-Filing: | | | | |
| Petition: | | | | |
| Patent-Appeals-and-Interference: | | | | |
| Post-Allowance-and-Post-Issuance: | | | | |
| Utility Appl issue fee | 1501 | 1 | 1400 | 1400 |
| Extension-of-Time: | | | | |

| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
|--------------------------|----------|----------|--------|----------------------|
| Miscellaneous: | | | | |
| Total in USD (\$) | | | | 1400 |